



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Jack H. Yuan, Eliyahou Harari, Yupin K. Fong and George Samachisa

Title: Scalable Self-Aligned Dual Floating Gate Memory Cell Array and
Methods of Forming the Array

Application No.: 10/799,180 Filing Date: March 12, 2004

Examiner: Igwe U. Anya Group Art Unit: 2812

Docket No.: SNDK.257US2 Conf. No.: 8152

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Sir:

This will respond to the Office Action mailed on July 12, 2004, setting a period
for response expiring on October 12, 2004.

Claim Amendments begin on page 2.

Drawing Amendments are not being made.

Specification Amendments are not being made.

Remarks begin on page 5.